jc996 U.S. PTO
10/092185
03/06/02

PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10092185	03/06/2002	209	573	3653	Rodriguez

**APPLICANTS: Beffa Raymond;

**CONTINUING DATA VERIFIED:
THIS APPLICATION IS A CON OF 09/941,092 08/28/2001 PAT 6,373,011
WHICH IS A CON OF 09/713,912 11/15/2000 PAT 6,365,861
WHICH IS A DIV OF 09/520,067 03/07/2000 PAT 6,350,959
WHICH IS A CON OF 09/133,338 08/13/1998 PAT 6,100,486
WHICH IS A DIV OF 08/785,353 01/17/1997 PAT 5,927,512
AND SAID 09/941,092 08/28/2001
IS A DIV OF 09/133,336 08/13/1998 PAT 6,147,316

BEST AVAILABLE COPY

FOREIGN APPLICATIONS VERIFIED: N/A 9/29/04

Formal Drawings (8 shts) set 1 3/6/02

PG-PUB	DO NOT PUBLISH ☐	RESCIND ☐

Foreign priority claimed ☐ yes ☑ no
35 USC 119 conditions met ☐ yes ☒ no
Verified and Acknowledged Examiners's intials

ATTORNEY DOCKET NO 3037.10US (95-1074.10)

TITLE: Method for sorting integrated circuit devices

U.S.DEPT. OF COMM./PAT.& TM-PTO-435L(Rev. 12-94)

[redacted] DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
8	8	6	3	1

(Assistant Examiner) 9/22/04 (Date)

NOTICE OF ALLOWANCE MAILED [redacted]

DONALD P. WALSH
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 3600
(Primary Examiner) 9/22/04 (Date)

ISSUE FEE
Amount Due [redacted] Date Paid

(Legal Instruments Examiner) 9/21/04 (Date)

ISSUE BATCH NUMBER

NOTICE
Amount

(Attached in pocket on right inside flap)